



Communiqué de presse

Menta SAS Appoints Nassim Abderrahmane as AI Product Manager

Sophia-Antipolis, June 27th, 2023 – Menta, a French deeptech at the forefront of next-gen chipsets, strengthens its technology team with an AI expert.

Specialized engineer in Embedded Systems and Information Processing, Nassim has an electronics PhD. His doctoral dissertation on the hardware implementation of AI algorithms from the Université Côte d'Azur, has generated several reference publications.



Nassim then joined the IRT Saint Exupéry Technology Research Center in Sophia-Antipolis, focused on aeronautics, space and embedded systems, first as a postdoctoral researcher and then as a research engineer in embedded artificial intelligence. In this position, he worked on two projects contributing to the design of neuromorphic architectures for the hardware deployment of AI algorithms, notably for satellite data analysis, on FPGA platforms, under strong constraints of energy consumption and robustness.

« Artificial intelligence is revolutionizing the semiconductor industry, with algorithms evolving 4 times faster than the production rate of a chip. Our technology is a response to the problem of obsolescence induced by Artificial Intelligence. The creation of an AI product manager position was a strategic challenge for Menta, and Nassim has the perfect profile to fit the job. His highly specialized technical expertise and his personality make him a key person not only for Menta, but also for the various European projects in which we are involved, such as Nimble AI » stated Vincent Markus, CEO of Menta.

Within an embedded AI development and context, Menta's eFPGA intellectual property (IP) represents a highly promising solution for accelerating and deploying artificial neural networks. Indeed, integrating Menta's technology into an SoC will improve its performance and power consumption into the neural network inference, notably by accelerating the most demanding parts of these algorithms, such as the matrix multiplication accumulation operations. Moreover, the flexibility of its eFPGA IP, allowing reconfigurability and programming during the lifecycle of the chiplet, as well as advantages in terms of performance and low power consumption, offer a significant advantage in AI deployment. This is particularly valuable for embedded AI applications, because data and requirements change frequently, and the AI field is constantly evolving.

« I'm particularly thrilled to join Menta, which is today the only European player in embedded programmable logic and is involved in a number of major European projects. This is particularly the case with NimbleAI, where Menta is contributing its technological expertise to the design of a 3D neuromorphic chip. It's particularly exciting to be involved in a project that meets European ambitions, and I'm proud to be able to carry out the mission of building an ecosystem of partners exploiting Menta's eFPGAs for artificial intelligence » concluded Nassim Abderrahmane.

About [Menta](#):

Menta is a privately held company based in Sophia-Antipolis (France). Menta is a proven pioneer of eFPGA for ASIC and SoC designers seeking speed, accuracy, performance and efficiency. eFPGA's adaptable architecture, based on design-adaptive standard cells-based and a state-of-the-art tool set, provides the highest degree of design customization, best-in-class testability, and fastest time-of-volume for SoC design at any foundry.

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