

Menta Exhibits Solutions Designed to Revolutionize the eFPGA Market at DAC 2022

The proven eFPGA pioneer and Winner of the Best in Show 2022 by Embedded Computing Design for its eFPGA Soft IP technology at Embedded World Germany presents joint solutions with partners and group demonstrations.

Today, Menta, the proven eFPGA pioneer, whose design-adaptive standard cells-based architecture and state-of-the-art tool set provides the highest degree of design customization, best-in-class testability and fastest time-to-volume for SoC design targeting any production node at any foundry, is demonstrating a variety of state-of-the-art solutions designed to revolutionize the eFPGA market at the Design Automation Conference (DAC) 2022.



At this 59th edition of DAC, Menta will present an eFPGA IP prototype that extends the capabilities of RISC-V

embedded processors with German provider of processor and IP design solutions Codasip. Menta will also unveil in a joint presentation with its partner Andes Technology, the Custom Extension™ Feature implemented on Menta eFPGA for RISC-V Cores ISA Reconfigurability.

In addition to these demonstrations, Menta will showcase technical theater presentations of the company's latest products and partnership solutions. This includes Menta eFPGA Soft IP, a product changing how eFPGAs are designed to meet customer requirements on any standard cell technology. The company will also present Radiation Hardened eFPGA solutions, embedded security solutions and more.

Menta, whose eFPGA Soft IP technology was chosen as a Best in Show product at Embedded World in Nuremberg last month, offers SoCs, ASICs and sensors designers the means to update their chips over time and in the field for applications such as security/cryptography, artificial intelligence, motor control and telecommunication. Menta products are used worldwide by companies serving the defense, space, Internet of things, industrial, 5G and automotive markets.

The design-adaptive standard cells-based architecture and state-of-the-art tool set provides the highest degree of design customization, best-in-class testability and fastest time-to-volume for SoC design targeting any production

node at any foundry. Menta eFPGA IP is uniquely offered both as a hard IP (GDSII) and as a soft IP (RTL). It has been taped-out on eight technology nodes at four different foundries and is in mass production.

Menta eFPGA technical experts will be on site to discuss the company's solutions, answer attendee questions and share how customers are using Menta eFPGA to gain competitive advantage in a range of applications.

DAC 2022 attendees will have the opportunity to learn more about Menta's strategic partnerships with Codasip and Andes Technology, both leading suppliers of high efficiency, low-power 32/64-bit RISC-V processor cores and a founding premier member of RISC-V International. Product rollouts and group demonstrations are scheduled to take place in Menta Booth #1346 on Monday, July 11 through Wednesday, July 13 from 11am-4pm PDT.

In addition to Codasip and Andes Technology, Menta's partners ecosystem includes Everspin Technologies, Secure-IC, CHX, Thales Alenia Space and Trusted Semiconductor Solutions.

Menta will be exhibiting at DAC 2022 in San Francisco on July 11-13. Anyone interested in learning more about Menta can visit Booth #1346 or arrange a private meeting or demo by contacting the Menta team at: info@menta-efpga.com.



"We're very excited to exhibit at DAC 2022 and welcome the opportunity to return to an in-person event. With our flourishing partnership with Trusted Semiconductor Solutions announced at

GOMAC Tech event this year and presentations with our partners Andes & Codasip, we have so much to share with the attendees this year. This includes our current efforts to revolutionize the eFPGA market."

Vincent MARKUS, Menta Inc.
President and chief executive officer

ABOUT MENTA

Menta SAS, based in Sophia-Antipolis, France, is a private semiconductor IP provider offering a ground-breaking performance accelerator for edge computing in the form of embedded FPGA (eFPGA) IPs. Menta SAS is the choice for ASICs and SoCs designers who need fast, right-the-first time design, and fast time to volume. Menta is the proven eFPGA pioneer whose design-adaptive standard cells-based architecture and state-of-the-art tool set provides the highest degree of design customization, best-in-class testability, and fastest time-to-volume for SoC design targeting any production node at any foundry. Menta's eFPGAs are available as Soft RTL or Hard GDSII IP. Menta products are used worldwide by companies serving the defense, space, IoT, AI, 5G or automotive markets. Menta fosters the vision of a world connected through distributed computing and the company stands as a catalyst of this evolution.

More informations: menta-efpga.com 