



Menta SAS and CEA-LIST present the MOSAICS-LP project at the 3D & Systems Summit

Dresden Hilton Hotel (Germany) - June 26-28, 2023

Sophia-Antipolis, June 8th, 2023 - CEA-LIST and Menta will jointly present the MOSAICS-LP project, a scalable platform for the design of heterogeneous chiplet-based chips for edge computing markets.

A challenge for European sovereignty

The European Chips Act sets a goal to increase the production capacity of chips in Europe from 10% to 20%. Europe will therefore significantly increase its chip production capacity. This would reinforce its sovereignty in microelectronics. However, this objective cannot be reached by building new semiconductor foundries only. It also requires offering European chip makers the possibility to lower the design cost and reduce the production time, especially in the markets where Europe is strong: edge and deep edge computing, mobility and defense. Those markets are in addition all affected by component shortages. A way forward is the use of chiplets for heterogeneous chips design.

"The production of heterogeneous chips is a major issue of European sovereignty, and the MOSAICS-LP project, which we are leading in cooperation with the CEA-List, is designed to increase the European Union's semiconductor design objectives tenfold, with minimal investment compared with fab construction. The stakes involved in this project are of crucial importance for the entire microelectronics industry," said Vincent Markus, CEO of Menta.

A platform for a multi-source chip ecosystem

The presentation will focus on system architecture and innovation. It benefits from CEA's experience in system design, chip fabrication and packaging, as well as hardware reconfiguration within the system thanks to Menta's eFPGA IPs.

Supply chain aspects and the business model will also be discussed.

In session 4 on chip packaging: architectures and challenges, Menta and CEA-LIST will present:

June 27 - 2.10 - 2.30 pm: "MOSAICS-LP: A Scalable Chiplet Platform" by Anthony Philippe, Research Engineer at CEA-LIST, and Yoan Dupret, CTO at Menta.

About [Menta](#):

Menta is a privately held company based in Sophia-Antipolis (France). Menta is a proven pioneer of eFPGA for ASIC and SoC designers seeking speed, accuracy, performance and efficiency. eFPGA's adaptable architecture, based on design-adaptive standard cells-based and a state-of-the-art tool set, provides the highest degree of design customization, best-in-class testability, and fastest time-of-volume for SoC design at any foundry.

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